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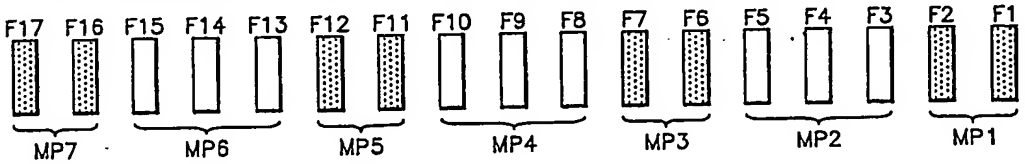
(54) Motion detection in television signals

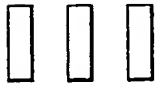
(57) A method and apparatus are provided for detecting image motion in a television signal obtained by conversion of film images to a sequence of television video fields using a 2-3 film pulldown technique, comprising generating a plurality of interim motion detection signals which are always based upon a comparison of image points in the video signals which are spaced two sequential fields apart, and gating the plurality of interim motion detection signals together in order to generate a final motion detection signal. The gating step preferably comprises generating all but one of the plurality of interim motion detection signals by delaying the prior outputs of a single motion detector and combining them with an AND function and combining the output of the AND function and the remaining one of the interim motion detectors with an OR function.



MP1,....MP7 CONSECUTIVE MOTION PICTURE FRAMES

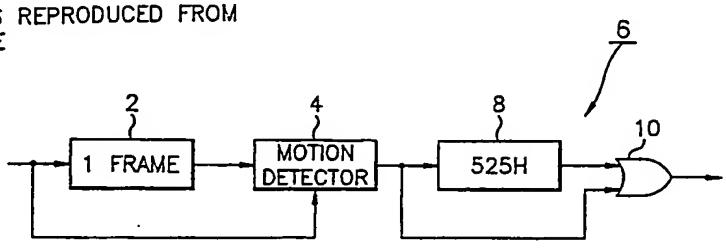
F1,....F17 CONSECUTIVE TELEVISION FIELDS

SET OF TWO TELEVISION FIELDS REPRODUCED FROM A SINGLE MOTION PICTURE FRAME

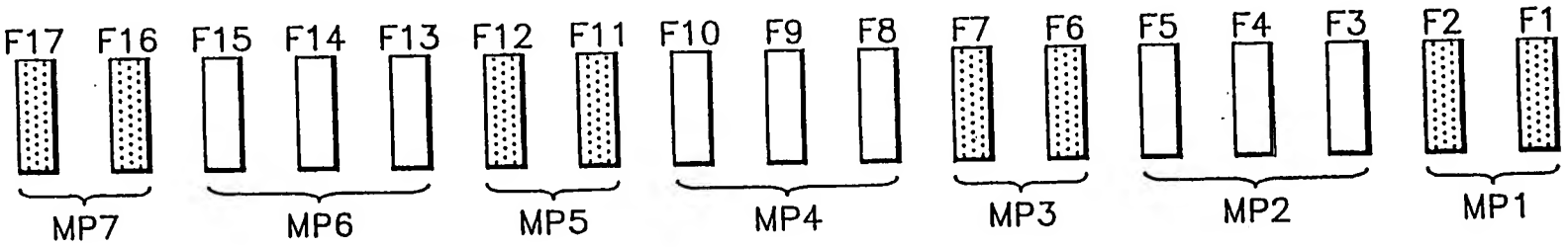


SET OF THREE TELEVISION FIELDS REPRODUCED FROM A SINGLE MOTION PICTURE FRAME

FIG. 1

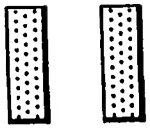


GB 2 258 580

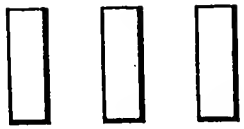


MP1,....MP7 CONSECUTIVE MOTION PICTURE FRAMES

F1,....F17 CONSECUTIVE TELEVISION FIELDS



SET OF TWO TELEVISION FIELDS REPRODUCED FROM A SINGLE MOTION PICTURE FRAME



SET OF THREE TELEVISION FIELDS REPRODUCED FROM A SINGLE MOTION PICTURE FRAME

FIG. 1

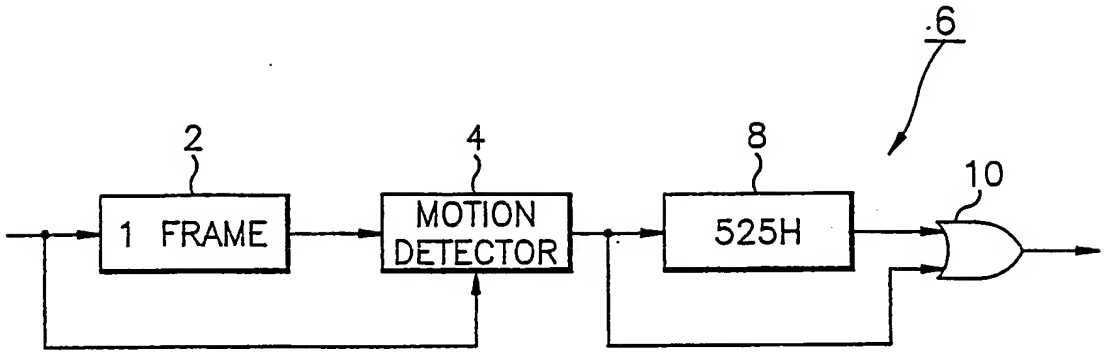


FIG. 2

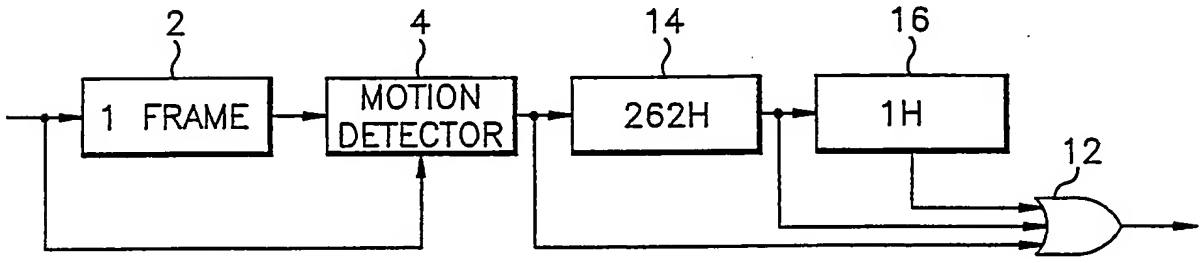


FIG. 3

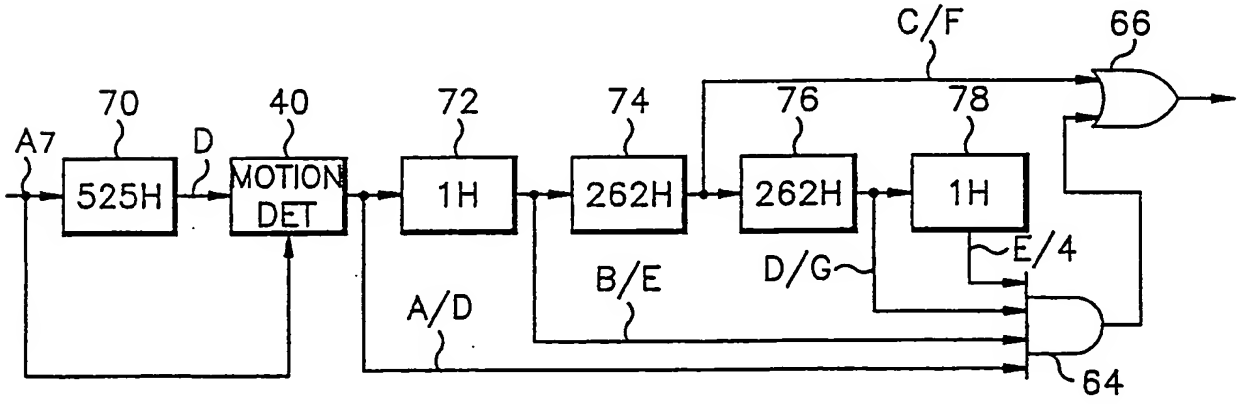


FIG. 7

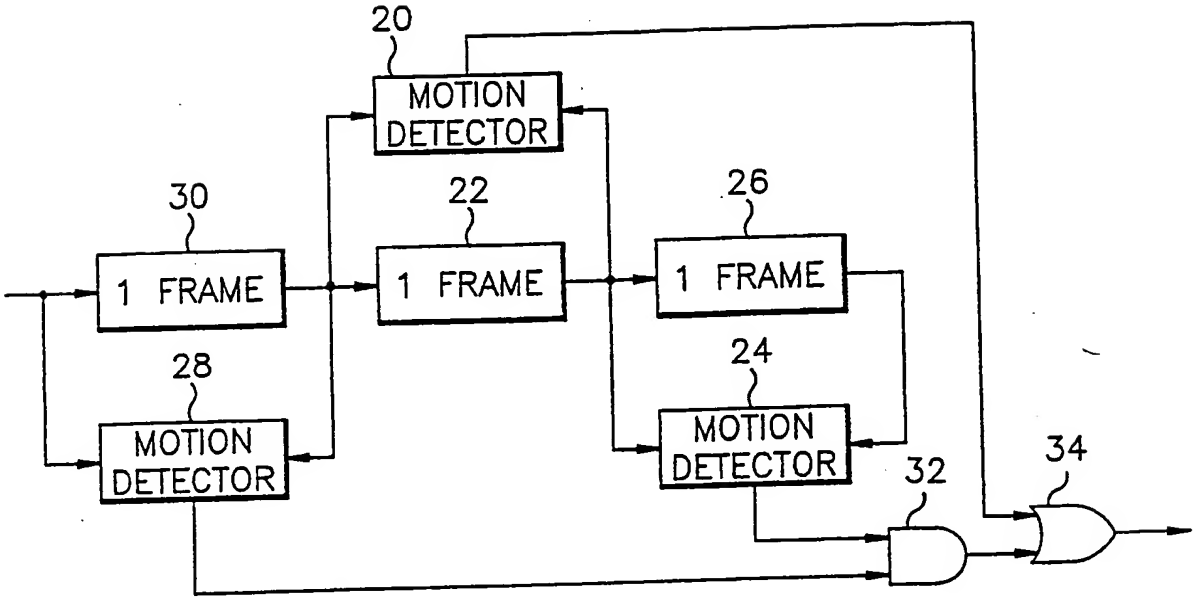

20
MOTION DETECTOR
30
1 FRAME
22
1 FRAME
26
1 FRAME
28
MOTION DETECTOR
24
MOTION DETECTOR
32
34

FIG. 4

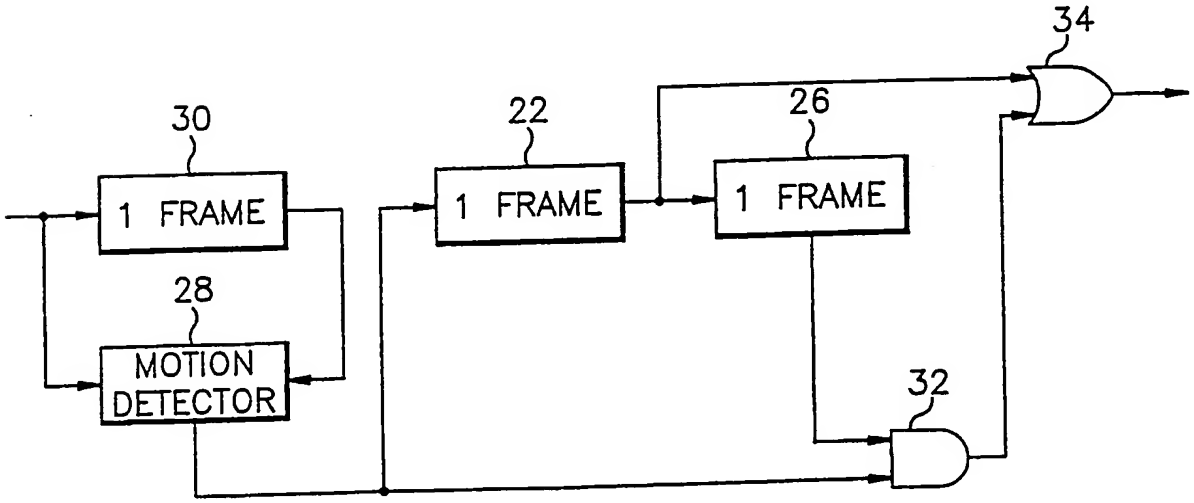

34
30
1 FRAME
22
1 FRAME
26
1 FRAME
28
MOTION DETECTOR
32

FIG. 5

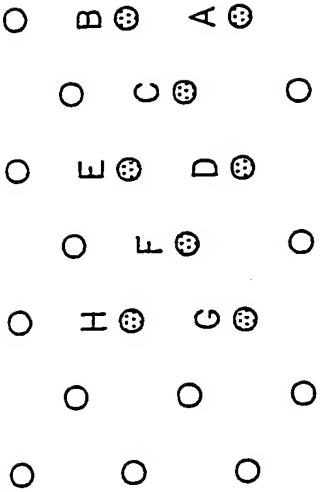

B
A
C
E
D
F
H
G

FIG. 6A

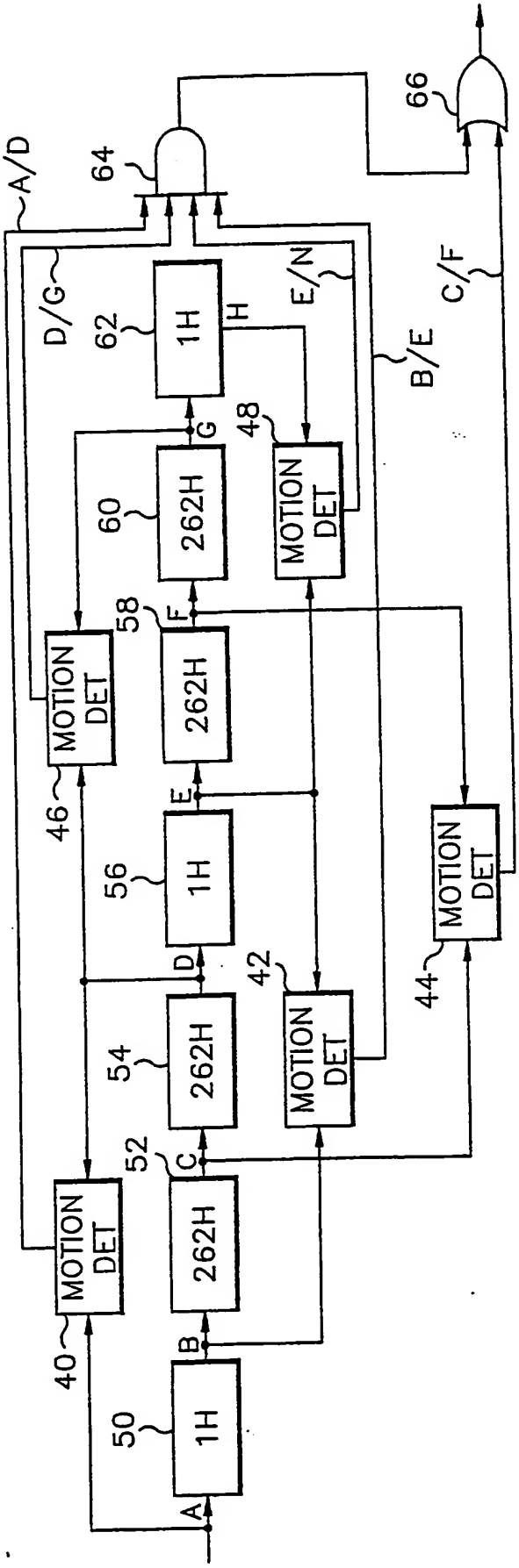

A/D
64
66
D/G
62
1H
H
E/N
B/E
C/F
G
48
60
262H
MOTION DET
58
F
46
56
1H
E
D
42
54
262H
MOTION DET
44
52
C
40
50
1H
262H
MOTION DET
B
A

FIG. 6B

MOTION DETECTION IN TELEVISION SIGNALS

The invention relates to a method and apparatus of motion detection which is particularly well suited to detect motion in a television signal which has been obtained from a film-to television converter using a conventional 2-3 pull-down technique.

Motion picture film is normally exposed at a rate of 24 frames per second. When reproducing these films in a television system with a frame rate of 25 frames per second (such as PAL or SECAM) it is universal practice to simply reproduce the 24-frame rate film at 25 frames per second. The small increase in the pitch of the reproduced sound and the slight reduction in the running time (i.e. speed-up) of the film have been a generally accepted compromise. The use of this method in a 30 frame per second television system (such as NTSC), however, is totally unacceptable. This 30 frame per second system uses a special method in which one motion picture frame is exposed for two television fields, whereas the next motion picture frame is exposed for three television fields. This method of alternately holding the film in the projector gate for either two or three television field exposures is called 2-3 pulldown.

Although 2-3 pulldown has worked well in the past, it can cause problems when digital signal processing methods are applied to the television signal. For example, many digital television systems require an accurate motion detector in order to select a proper signal processing algorithm, e.g. for deciding whether or not to use a line or frame comb filter for luminance/chrominance signal separation. Known motion detectors for this purpose generally compare two picture elements that are a frame (two fields)

apart and base their motion detection decision on the difference in signal amplitude between these two picture elements. If the television picture is obtained from a motion picture film and a 2-3 pulldown has been used for the frame-rate conversion, it has been found, as will be explained in greater detail below, that the motion detector will fail regularly, which may lead to a degradation of picture quality. If, for example, the motion detection signal was being used for controlling the use of either a line or a frame comb filter in a television signal processor, a regularly occurring failure of the motion detector (i.e. an indication of no motion when in fact there is motion) will result in a periodic switching between these two comb filter techniques. This leads to a visible periodic flicker in the resolution of the television picture, as explained in more detail below, due to the different resolutions resulting from the use of a line or field comb filter.

To understand more clearly why a conventional motion detector will fail, consider Figure 1 of the accompanying diagrammatic drawings. Firstly, assume that each of the motion picture frames is different. A motion detector comparing corresponding picture elements separated by a frame interval will detect motion between, e.g. corresponding picture elements in fields 4 and 6 (F4/F6). Similarly, it will detect motion between corresponding picture elements in fields F5/F7, F6/F8, and F7/F9. It will not detect motion, however, when it compares picture elements from fields F8/F1O since, although these picture elements are a television frame apart, both these fields correspond to the same motion picture frame (MP 4) and are thus spatially identical. Continuing this evaluation of the motion detector, one can easily see that the motion detector will indicate "no motion" every five television fields although all motion picture frames are different. This wrong decision can lead to the selection of the wrong luma/chroma signal separation

algorithm at a regularly occurring low frequency rate, i.e. 12Hz, and thus result in visually annoying artifacts in the displayed picture due to the difference in signal resolution between line and field comb filtering.

U.S. Patent 4,933,759 uses a plurality of field delay circuits to perform various combinations of signal comparisons in order to develop a motion detection signal. For ease of understanding, the sequential fields (n-2) through (n+3) of Figure 1 of that patent will be referred to as F1-F6, respectively. Thus, comparators 1 and 2 of Figure 2 of that patent are responsive to signal in fields F1, F3, F4 and F3, F4, F5, and then F2, F3, F4 and F3, F4, F6, respectively, when switches 1 and 2 are switched to their alternate positions.

Due to the fact that in the above-noted comparisons, adjacent fields are being compared, i.e. fields F3/F4; F3/F4/F5; F2/F3/F4; and F3/F4, and the presence of line interlace, a 4-point averaging technique is required to spatially match a point in one field with a corresponding point in an adjacent field (due to the interlace "line offset").

This necessarily complicates the comparator functions and also, due to the possibility of e.g. a black/white transition occurring in the image space between adjacent field lines, which would result in a gray output from the comparator, the threshold required to indicate motion would have to be set a relatively high value, thereby increasing the chance of an indication of no motion when in fact there was motion.

In preferred embodiments of the present invention, a plurality of television interim motion detection signals are developed by always comparing

image points in the video signal which are spaced two sequential fields apart, and then gating the interim motion detection signals together in order to generate a final motion detection signal.

In a preferred aspect of the invention, all but one of the interim motion detection signals are combined using an AND function, and the output of the AND function and the remaining interim motion detection signal are combined using an OR function.

In accordance with a still further preferred aspect of the invention, the interim motion detection signals combined using the AND function correspond to a comparison of image points in the television image which are spatially adjacent to the image points used to develop the remaining interim motion detection signal.

In accordance with one embodiment of the invention, the interim motion detection signals are all developed by a single motion detector, whose output is successively delayed for providing the plurality of interim motion detection signals to a gating circuit arrangement.

According to one aspect of the present invention, there is provided a method for detecting image motion in a television signal obtained by conversion of film images to a sequence of television video fields using a 2-3 film pulldown technique, the method comprising the steps of:

generating a plurality of interim motion detection signals which are always based upon a comparison of image points in the video signal which are spaced two sequential fields apart; and

gating said plurality of interim motion detection signals together in order to generate a final motion detection signal.

Preferably, said generating step comprises:

generating all but one of said plurality of interim motion detection signals which are gated, by delaying the prior outputs of a single motion detector.

Preferably, said gating step comprises:

combining all but one of said interim motion detection signals together with an AND function and combining the output of the AND function and the remaining one of said interim motion detection signals together with an OR function.

According to another aspect of the present invention, there is provided apparatus for detecting image motion in a television signal obtained by conversion of film images to a sequence of television video fields using a 2-3 film pulldown technique, the apparatus comprising:

generating means for generating a plurality of interim motion detecting signals, each interim motion detection signal always being based upon a comparison of image points in the video signal which are spaced two sequential fields apart; and

gating means for gating said plurality of interim motion detection signals together in order to generate a final motion detection signal.

Preferably, said gating means comprises:

an OR gate having at least two signal inputs, a first input being responsive to an undelayed output of said generating means and said second input being responsive to a delayed version of said interim motion detection signal.

Alternatively, said gating means comprises:

an AND gate for combining all but one of said plurality of interim motion detection signals together; and

an OR gate for combining the output of the AND function and the remaining one of said interim motion detection signals together.

Preferably, said generating means generates surrounding interim motion detection signals based on comparisons in video signal lines in adjacent frames that are spatially positioned above and below (surrounding) a given line position; and said surrounding interim motion detection signals are combined in said AND gate, the output of which is combined with the interim motion detection signal for said given line position in said OR gate.

Preferably, said generating means comprises:

a frame delay;

a motion detector having first and second signal inputs and a motion detector output for providing an interim motion detection signal, said video

signal being coupled to the inputs of said frame delay and said first input of said motion detector, and the output of said frame delay being coupled to said second input of said motion detector; and a second frame delay coupled to the output of said motion detector for delaying said interim motion detector signal one frame time period so that it is provided to said gating means concurrently with the next sequential interim motion detection signal provided by said motion detector.

An apparatus as above may further include:

a third frame delay coupled to the output of said second frame delay, for providing a further interim motion detection signal to said gating means.

The invention extends to a video signal processing apparatus provided with apparatus, or adapted to perform a method, according to any of the preceding aspects of the invention.

Such apparatus may comprise a television receiver and/or video signal recording and/or reproducing means.

For a better understanding of the invention, and to show how the same may be carried into effect, reference will now be made, by way of example, to the accompanying diagrammatic drawings, in which:

Figure 1, as mentioned above, illustrates the result of the application of a 2-3 pulldown film-to-television frame-rate conversion technique;

Figure 2 illustrates one example of a basic circuit employing the principles of the present invention;

Figure 3 illustrates a further embodiment of the invention having a different technique for gating together interim motion detection signals;

Figure 4 illustrates a circuit for more accurately determining the occurrence of motion, in accordance with a further embodiment of the invention;

Figure 5 illustrates a simplified version of the Figure 4 embodiment;

Figure 6a illustrates the lines of a television field and is useful for understanding the embodiments shown in Figure 6b and 7;

Figure 6b illustrates a further embodiment of the invention; and

Figure 7 illustrates a simplified version of the Figure 6b embodiment.

A simple motion detection circuit arrangement which can be used, for example, in a television receiver for controlling whether or not a line or frame comb filter will be used for a luma/chroma separation circuit, is shown in Figure 2. The remainder of the television receiver, including the luma/chroma separation circuit and the remainder of the television signal processing and display circuitry, is not shown for ease of understanding the invention and for clarity of the drawings. It is noted that all such components which are not shown are of the type which are well known to those of ordinary skill in the art.

As shown in Figure 2, the television signal is applied to the input of a one-frame delay 2. The output of frame delay 2 is supplied as a first signal input to a motion detector 4. The second input to detector 4 is the undelayed television signal. Motion detector 4 comprises a conventional type of motion detector, such as one which compares the signal level of two pixels simultaneously presented at its respective inputs and determines if their difference exceeds a preset threshold level. If the difference between the amplitude level of the signal input is less than the threshold, motion detector 4 provides an output indicating no motion. The output of motion detector 4 is able to change at the pixel rate, thereby providing a plurality of interim motion detection signals, each motion detection signal being based upon a comparison of pixels which are exactly one frame apart in the television signal. A gating circuit 6 is used to gate the interim motion detection signals together in order to provide a final motion detection signal.

In the Figure 2 embodiment, gating circuit 6 includes a frame delay 8 and an OR gate 10. In the NTSC system, frame delay 8 comprises a delay of 525 horizontal (H) lines. When detector 4 provides a current motion detection signal to OR gate 10, delay 8 provides the motion detection signal to OR gate 10 which was generated one frame ago. For example, when detector 4 provides a signal based upon a pixel comparison in F5/F7, delay 8 provides a signal based upon a comparison one frame earlier, i.e. F3/F5. Thus, when the erroneous F3/F5 detection signal (indicating "no motion" and having a LOW logic level) is applied to gate 10, the correct F5/F7 signal (indicating "motion" and having a HIGH logic level) which is also applied, will dominate OR gate 10, thus preventing the final output signal from being in error.

Figure 3 illustrates a motion detector which is substantially similar to that shown in Figure 2. However, a more reliable transition between a "motion" and a "no motion" signal is provided. This is accomplished by a gating circuit comprising a three-input OR gate 12, for receiving successive interim motion detection signals provided from the output of motion detector 4. A first interim motion detection signal is directly applied to gate 12, a second interim motion detection signal is applied to gate 12 via a 262H delay 14, and a third interim motion detection signal is applied to gate 12 via the 262H delay 14 plus a 1H delay 16. In this manner, the final motion detector signal, the output of OR gate 12, is a combination of, e.g. an F1O/F8 and two F9/F7 interim motion detection signals. There are two F9/F7 signals due to the action of 1H delay 16, which two signals indicate if there is motion in the lines spatially above and below the current line being compared in F1O/F8.

Another circuit for accurately determining motion is shown in Figure 4. Referring again for a moment to Figure 1, assume that a simple motion detector is comparing pixels in frames F1O and F8. Since the picture elements in F1O and F8 are identical, it will not detect motion even if MP 4 is different from MP 3 and MP 5. The motion detector of Figure 4 not only makes the F8/F1O comparison via detector 20 and frame-delay 22, but also the F8/F6 comparison via detector 24 and frame-delay 26 to check for motion between MP 3 and MP 4, and the F1O/F12 comparison via detector 28 and frame-delay 30 to check for motion between MP 4 and MP 5. An AND gate 32 receives the outputs of detectors 24 and 28, and an OR gate 34 receives the output of AND gate 32 and detector 20.

Logic gates 32,34 determine motion in the following way. If motion detector 20 indicates motion, then the output of OR gate 34 will indicate motion. If motion detector 20 indicates "no motion", then the output of gate 34 is determined by the outputs of motion detectors 24 and 28. Only if both of these motion detectors indicate motion, by a logical HIGH value, will the output of gate 32 be HIGH and the output of gate 34 indicate motion. It is noted that if only one of motion detectors 24 and 28 indicates motion, we are either at the start or the end of a motion scene.

Figure 5 performs exactly the same function as Figure 4. However, in this case the motion signal output from detector 28 (and not the video signal) is merely delayed by two additional frames. With this arrangement, the same function is accomplished with less hardware. That is, the output of delays 22 and 26 provide motion detection signals which correspond to the outputs of detectors 20 and 24, respectively, of the Figure 4 embodiment. It is noted that this embodiment also provides significant reductions in bit storage requirements and processing because the motion detector output signal is typically one bit, while the video signal can be 8 bits or more.

Figure 6a illustrates some of the lines of successive television fields, wherein lines used in the following illustrated examples are shown shaded and given reference designations.

Figure 6b shows another embodiment of the invention including a plurality of motion detectors 40, 42, 44, 46 and 48. A series connection of delay circuits 50, 52, 54, 56, 58, 60 and 62 are grouped so as to provide a single frame delay between the inputs of each of detectors 40-48, with each frame delay comprising a 1-line delay and two 262-line delays. With this

arrangement, additional lines above and below the current line (but in different fields) are used to determine motion. For example, assume that line C is the current line and will be displayed or processed next. If motion detector 44 indicates that there is no difference between corresponding pixels in line C and line F (the line which occurred one frame earlier), then motion will only be indicated if all other motion detectors 40, 42, 46 and 48 indicate motion, i.e. if respective pixel comparisons in lines A/D, B/E, D/G and E/H indicate motion. An AND gate 64 receives the outputs of detectors 40, 42, 46, and 48 and an OR gate 66 receives the output from AND gate 64 and detector 44 for generating the final motion detection signal in a manner such as previously described with respect to the prior Figures.

Figure 7 performs the same function as Figure 6b, but requires considerably less hardware than the circuit in Figure 6b due to successive delaying of the motion detection signal instead of the video signal (such as in the Figure 5 simplification of Figure 4). Here, a frame delay 70 cooperates with detector 40 for providing, e.g. the A/D signal, a one-line delay 72 provides the B/E signal, a 262- line delay 74 provides the C/F signal, a 262-line delay 76 provides the D/G signal and a 1-line delay 78 provides the E/H signal. Gates 64 and 66 operate in the same manner as shown and described for Figure 6b.

Thus, there has been shown and described a novel motion detector which fulfils all the objects and advantages sought therefore. Many changes, modifications, variations and other uses and applications of the subject invention will, however, become apparent to those skilled in the art after considering this specification and its accompanying drawings, which disclose preferred embodiments thereof. In an alternative embodiment, by substituting

AND-gate 64 which is shown in Figure 6b with a PROM, one can easily modify the gating requirement for the motion detection signal. One possibility is that only two of the pairs, either (E,H) and (B,E) or (D,G) and (A,D), have to indicate motion in order for the final output to indicate motion. All such changes, modifications, variations and other uses and applications which do not depart from the spirit and scope of the invention are deemed to be covered by the invention.

The reader's attention is directed to all papers and documents which are filed concurrently with or previous to this specification in connection with this application and which are open to public inspection with this specification, and the contents of all such papers and documents are incorporated herein by reference.

All of the features disclosed in this specification (including any accompanying claims, abstract and drawings), and/or all of the steps of any method or process so disclosed, may be combined in any combination, except combinations where at least some of such features and/or steps are mutually exclusive.

Each feature disclosed in this specification (including any accompanying claims, abstract and drawings), may be replaced by alternative features serving the same, equivalent or similar purpose, unless expressly stated otherwise. Thus, unless expressly stated otherwise, each feature disclosed is one example only of a generic series of equivalent or similar features.

The invention is not restricted to the details of the foregoing embodiment(s). The invention extends to any novel one, or any novel combination, of the features disclosed in this specification (including any accompanying claims, abstract and drawings), or to any novel one, or any novel combination, of the steps of any method or process so disclosed.

CLAIMS

1. A method for detecting image motion in a television signal obtained by conversion of film images to a sequence of television video fields using a 2-3 film pulldown technique, the method comprising the steps of:

generating a plurality of interim motion detection signals which are always based upon a comparison of image points in the video signal which are spaced two sequential fields apart; and

gating said plurality of interim motion detection signals together in order to generate a final motion detection signal.

2. A method according to claim 1, wherein said generating step comprises:

generating all but one of said plurality of interim motion detection signals which are gated, by delaying the prior outputs of a single motion detector.

3. A method according to claim 1 or 2, wherein said gating step comprises:

combining all but one of said interim motion detection signals together with an AND function and combining the output of the AND function and the remaining one of said interim motion detection signals together with an OR function.

4. Apparatus for detecting image motion in a television signal obtained by conversion of film images to a sequence of television video fields using a 2-3 film pulldown technique, the apparatus comprising:

generating means for generating a plurality of interim motion detecting signals, each interim motion detection signal always being based upon a comparison of image points in the video signal which are spaced two sequential fields apart; and

gating means for gating said plurality of interim motion detection signals together in order to generate a final motion detection signal.

5. Apparatus according to claim 4, wherein said gating means comprises:

an OR gate having at least two signal inputs, a first input being responsive to an undelayed output of said generating means and said second input being responsive to a delayed version of said interim motion detection signal.

6. Apparatus according to claim 4, wherein said gating means comprises:

an AND gate for combining all but one of said plurality of interim motion detection signals together; and

an OR gate for combining the output of the AND function and the remaining one of said interim motion detection signals together.

7. Apparatus according to claim 6, wherein:

said generating means generates surrounding interim motion detection signals based on comparisons in video signal lines in adjacent frames that are spatially positioned above and below (surrounding) a given line position; and

said surrounding interim motion detection signals are combined in said AND gate, the output of which is combined with the interim motion detection signal for said given line position in said OR gate.

8. Apparatus according to claim 4 or 5, wherein said generating means comprises:

a frame delay;

a motion detector having first and second signal inputs and a motion detector output for providing an interim motion detection signal, said video signal being coupled to the inputs of said frame delay and said first input of said motion detector, and the output of said frame delay being coupled to said second input of said motion detector; and a second frame delay coupled to the output of said motion detector for delaying said interim motion detector signal one frame time period so that it is provided to said gating means concurrently with the next sequential interim motion detection signal provided by said motion detector.

9. Apparatus according to claim 8, further including:

a third frame delay coupled to the output of said second frame delay, for providing a further interim motion detection signal to said gating means.

10. A method for detecting image motion in a television signal, the method being substantially as hereinbefore described with reference to the accompanying drawings.

11. Apparatus for detecting image motion in a television signal, the apparatus being substantially as hereinbefore described with reference to the accompanying drawings.

12. Video signal processing apparatus provided with apparatus, or adapted to perform a method, according to any of the preceding claims.

13. Apparatus according to claim 12, comprising a television receiver and/or video signal recording and/or reproducing means.

Patents Act 1977
Examiner's report to the Comptroller under Section 17 (The Search Report)

Application number
9209309.5

Relevant Technical fields

(i) UK Cl (Edition K) H4F (FEP,FEX,FGXX)

(ii) Int Cl (Edition 5) G06F; H04N

Search Examiner
A J RUDGE

Databases (see over)

(i) UK Patent Office

(ii) ONLINE DATABASES: WPI, INSPEC

Date of Search
13.08.92

Documents considered relevant following a search in respect of claims ALL

Category (see over)	Identity of document and relevant passages	Relevant to claim(s)
	NONE	

Category	Identity of document and relevant passages	Relevant to claim(s)

Categories of documents

X: Document indicating lack of novelty or of inventive step.

Y: Document indicating lack of inventive step if combined with one or more other documents of the same category.

A: Document indicating technological background and/or state of the art.

P: Document published on or after the declared priority date but before the filing date of the present application.

E: Patent document published on or after, but with priority date earlier than, the filing date of the present application.

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